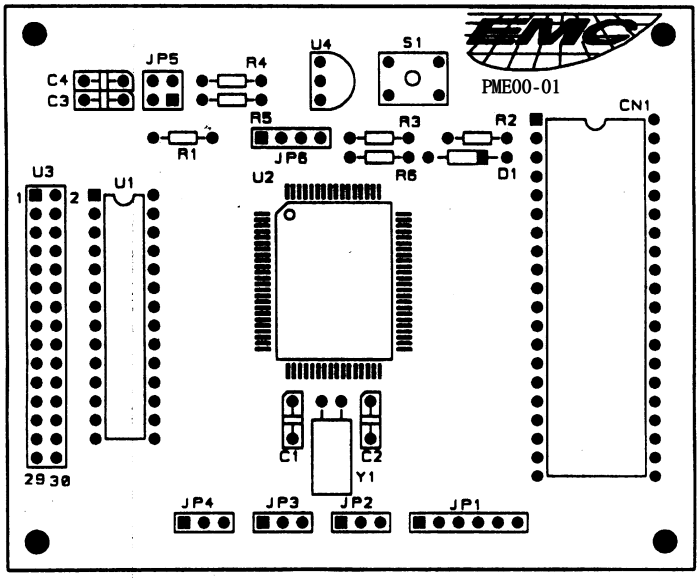
PM73E00 User's Guide

16-Jul-99

The PM73E00 is a processor module for EM73E00 used for in-circuit emulation and debug the application system.

- In the ICE mode : CN1 is ICE-73E ADAPTOR that connected to ICE.



- In the piggyback mode : An EPROM is put on CN1.

CN1

	1	40	
	2	39	
	3	38	
	4	37	
	5	36	
	6	35	
A15	7	34	VCC
A12	8	33	A14
A7	9	32	A13
A6	10	31	A8
A5	11	30	A9
A4	12	29	A11
A3	13	28	/OE
A2	14	27	A10
A1	15	26	/CE
A0	16	25	D7
D0	17	24	D6
D1	18	23	D5
D2	19	22	D4
VSS	20	21	D3

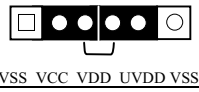
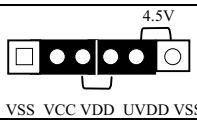
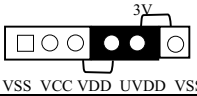
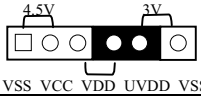
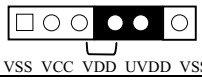
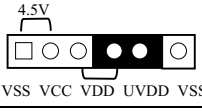
- U3 is connected to target board which mounted on system.

U3

P5.2	1	2	P5.1
P5.3	3	4	P5.0
P6.0	5	6	P4.3
P6.1	7	8	P4.2
P6.2	9	10	P4.1
P6.3	11	12	P4.0
P1.1	13	14	P0.3
RMO	15	16	P0.2
TEST	17	18	P0.1
UVDD	19	20	P0.0
XIN	21	22	P7.3
XOUT	23	24	P7.2
GND	25	26	P7.1
/RESET	27	28	P7.0
UVDD	29	30	GND

- The power source jumper is JP1. These pins defined as :
  VDD : power source for IC
  VCC : 5V power source from ICE or power source for EPROM
  UVDD : power source of user target board

- User can select three modes as following table.
  MODE 1 : Share same power with VCC that could from ICE or battery.
  MODE 2 : IC power stand alone(3V).
  MODE 3 : Use build-on power voltage regulator. User can get DC 3V for VDD.

Application / MODE	JP1		JP5
	ICE	PIGGYBACK	
MODE 1			
MODE 2			
MODE 3			

- The reset circuit (pull-up resistor and capacitor) are built on the processor module. It's not necessary to rebuild on the target board.

- If user haven't connected the oscillation elements on the target system, they must be on the processor module and JP4 be open as Fig.1 ~ Fig.2. If user have connected the oscillation elements on the target system, the elements (X1, R1) of processor module must be open and JP4 short as Fig.3 ~ Fig.5.

Oscillator Type	Oscillation Elements Placement			
	Processor Module		Target Board	
	Elements	Example	JP4	Example
Crystal Osc.	X1	Fig.1	X'TAL short	Fig.3
RC Osc.	R1	Fig.2	RC short	Fig.4
External Clk.	-	-	X'TAL short	Fig.5

Freq.	R1
2.3M	60.6K
3.7M	52.2K
4.7M	46.4K
5.5M	44.5K
6.7M	41.6K

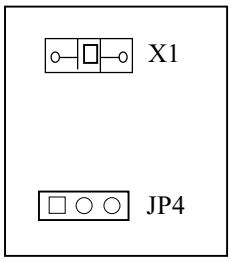


Fig. 1

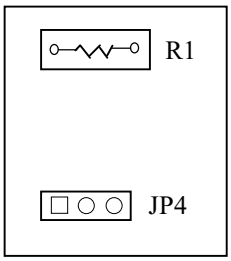


Fig. 2

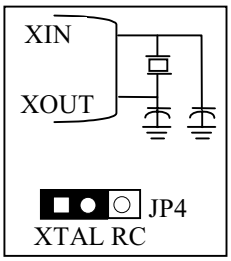


Fig. 3

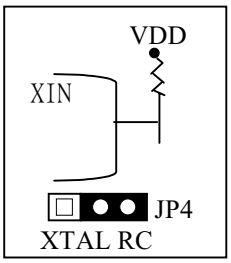


Fig. 4

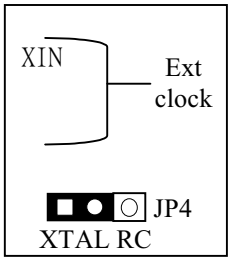


Fig. 5

- CN1 is the EPROM socket. The data in the EPROM is programmed by EPM file that is generated by OPTION EDITOR program (filename is LAYOUT).
  User must follow the range list in the table to program file into the EPROM.

27512 EPROM	Data Location
EPM	0000h~801F

Please refer the product specification of EM73E00 and ICE-73E manual for more detail information.